I B M FRANCE
Service des Etudes

Paris, le 12 Juin 1958

POSTCHEQUE PROCESSING SYSTEM

Preliminary specifications / engineering

I - BASIC UNITS

1 300 Doc. P.M. punched document Reader (designed by IBM Holland)

1 100 CPM Card Reader

1 100 CPM Summary Punch

1 300 LPM Printer with dual Feed (100 numerical printing positions - special characters - and *)

1 Processing unit

II - PROCESSING UNIT ENGINEERING SPECIFICATIONS

Technology

Identical to WWAM : core storage, voltage mode transistor circuits.

Data flow

Equivalent to WWAM, but only numerical : serial by digit, parallel by bit (1-2-4-8-C)

Core Storage

11 groups of 80 positions.
Each group may be splitted into 8 - 10 position words or 20 - 4 position words.

Core storage is divided into :

- Document read
  - 1 30 position buffer memory
  - 2 30 position Read memories
  - 2 30 position memories for not instanteneous reader stop.
- Card read
  - 2 80 position memories

- Card punch
  2 80 position memories

- Print
  1 100 position memory

- Working storage
  3 1/2 80 position memories

Program steps

80 sequential program steps. Their functions may be suppressed or modified, depending on the position of registers. These registers store :

- Result of calculation tests
- Type of document
- Result of comparison between document and card
- Result of comparison between successive documents
- Document account number, which gives the card zone to be processed.

Operations

Add - Reset and Add
Subtract - Reset and Subtract
Transfer
Comparison
Print Edit (Blank insert, - insert, zero suppress, protection asterisk)
Punch Edit (X over controlled)
Fee calculation subroutine

Mechanical operations

Card read, Card punch, Read document and Print are overlapped with calculation.

Speed

- Addition of 10 digit in 400 us
- Overall speed foreseen : 160 to 240 doc PM